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PROVISION OF INTERNAL CACHE STATUS FOR EXTERNAL CACHE MANAGEMENT

19-Oct-2001

IPCOM000005610D

English (United Kingdom)

Performance of a cache memory may be improved by allowing cache entries for multiple processes to reside in the cache concurrently. When a context switch occurs, the cache does not need to be automatically flushed of all entries. If the cache is large enough, upon returning ...

Result # 2 Relevance:



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System and method for automatically provisioning a telecommunications switch

12-Sep-2000

IPCOM000001778D

English (United Kingdom)

A system for providing country-specific data for a telecommunications switch is provided. The system includes a data storage system and a user interface. A controller is connected to the data storage system and the user interface. The controller is configured to retrieve country data ...

Result # 3 Relevance:



PREVIEW
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Method and apparatus for enabling firmware and device drivers of software of integrated graphics chipsets to share state and parameters

17-Jul-2001

IPCOM000005024D

English (United Kingdom)

Disclosed is a method and apparatus for enabling firmware and device drivers of software of integrated graphics chipsets to share state and parameters. Benefits include the capability for graphics controller and drivers to share graphics memory, improved hardware design, and ...

Result # 4 Relevance:



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GRAVITATIONAL SWITCHING DEVICE USED FOR READING AND/OR RESETTNG A COMMUNICATIONS DEVICE

22-Nov-2001

IPCOM000005989D

English (United Kingdom)

The standard method for reading and resetting communication devices involves a basic two hand operational sequence. Under normal operating conditions, this does not present an unusual problem, other than creating a basic inconvenience. The preferred method of operation ...

Result # 5 Relevance:



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this document

A MAPOS version 1 Extension - Switch-Switch Protocol (RFC2174)

13-Sep-2000

IPCOM000002732D

English (United Kingdom)

This document describes a MAPOS version 1 extension, SSP (Switch-Switch Protocol). MAPOS is a multiple access protocol for transmission of network protocol packets, encapsulated in High-Level Data Link Control (HDLC) frames over SONET/SDH. In MAPOS network, a SONET switch ...

Result # 6 Relevance:



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AUTO SWITCHING POWER SUPPLY

16-Nov-2001

IPCOM000005930D

English (United Kingdom)

Power supplies used in chargers are normally designed for 110 VAC or 220 VAC operation. They can be designed with either a different line transformer or a different power switcher ...

for 110 VAC or 220 VAC or a configuration with a split winding on the transformer with user selectable jumpers or a switch. ...

Result # 7

Relevance:  **PREVIEW**
this document**Cabletron's VlanHello Protocol Specification Version 4 (RFC2641)**

13-Sep-2000

IPCOM000003229D

English (United Kingdom)

The VlanHello protocol is part of the InterSwitch Message Protocol (ISMP), which provides interswitch communication between switches running Cabletron's SecureFast VLAN (SFVLAN) product. Switches use the VlanHello protocol to discover their neighboring switches and establish ...

Result # 8

Relevance:  **PREVIEW**
this document**Switch Settings on IMPs and TIPs (RFC0395)**

13-Sep-2000

IPCOM000003571D

English (United Kingdom)

This note provides a description of the switches on the front panel of IMP TIPs that are important to the correct operation of the network software. We have had some difficulty in the past when certain of these switches were incorrectly configured. This RFC will be updated as ...

Result # 9

Relevance:  **PREVIEW**
this document**Method for using the chipset AGP GART capability within the execution environment before the operating system is originated**

26-Feb-2002

IPCOM000007117D

English (United Kingdom)

Disclosed is a method for using the chipset accelerated graphics port (AGP Graphics Address Remapping Table (GART) capability within the execution environment before the operating system is originated. Benefits include improved performance.

Result # 10

Relevance:  **PREVIEW**
this document**Cisco Systems' Tag Switching Architecture Overview (RFC2105)**

13-Sep-2000

IPCOM000002659D

English (United Kingdom)

This document provides an overview of a novel approach to network layer packet forwarding, called tag switching. The two main components of the switching architecture - forwarding and control - are described. Forwarding is accomplished using simple label-swapping ...

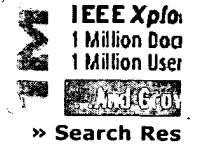
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Search query: task switching firmware update

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 Pages:143 - 145

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Relevance scale ☐ ☐ ☐ ☐ ☐1 [Switcherland: a QoS communication architecture for workstation clusters](#)

Hans Eberle, Erwin Oertli

 April 1998 **ACM SIGARCH Computer Architecture News , Proceedings of the 25th annual international symposium on Computer architecture**, Volume 26 Issue 3

Full text available:

[pdf\(1.32 MB\)](#)
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Computer systems have become powerful enough to process continuous data streams such as video or animated graphics. While processing power and communication bandwidth of today's systems typically are sufficient, quality of service (QoS) guarantees as required for handling such data types cannot be provided by these systems in adequate ways. We present Switcherland, a scalable communication architecture based on crossbar switches that provides QoS guarantees for workstation clusters in the form of ...

2 [Review: IBM eServer BladeCenter](#)

Dana Canfield

January 2004 **Linux Journal**, Volume 2004 Issue 117Full text available: [html\(10.42 KB\)](#) Additional Information: [full citation](#)3 [Stateful distributed interposition](#)

John Reumann, Kang G. Shin

February 2004 **ACM Transactions on Computer Systems (TOCS)**, Volume 22 Issue 1Full text available: [pdf\(833.84 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Interposition-based system enhancements for multitiered servers are difficult to build because important system context is typically lost at application and machine boundaries. For example, resource quotas and user identities do not propagate easily between cooperating services that execute on different hosts or that communicate with each other via intermediary services. Application-transparent system enhancement is difficult to achieve when such context information is obscured by complex service ...

Keywords: Distributed computing, component services, distributed context, multitiered services, operating systems, server consolidation

4 [ESP: a language for programmable devices](#)

Sanjeev Kumar, Yitzhak Mandelbaum, Xiang Yu, Kai Li

 May 2001 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2001 conference on Programming language design and implementation**, Volume 36 Issue 5



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... 3. If problems occur during the **firmware update** ... to be made using the ADMINISTRATOR function, and the device **firmware** has to ... Call up the NT **task** bar START ...

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[PDF] **A Firmware Monitor to Support Vertical Migration Decisions in the UNIX e Operating System" e**

B Holtkamp, H Kaestner - [Cited by 6](#)

... Thus the **firmware** monitor should count the number ... as the interval between two **update** operations between ... sequence can be in- terrupted by a **process switching**. ...

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Firmware basics for the boss, part 2

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... if you really need a project **update** and Shirley ... like the customer does, not as the **firmware** folks do ... and higher levels of disciplined software **process** lets one ...

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... example, data input may pre- empt a display **update task**, because collecting ... use them for seeing the presence of **task** execution and ... Have a **firmware/software** stan ...

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HG Okuno, N Osato, I Takeuchi, T Musashino - [Cited by 1](#)

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Proceedings of the 20th annual workshop on Microprogramming, 1987 - [portal.acm.org](#) - [portal.acm.org](#)

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N Kamibayashi, H Ogawana, K Nagayama, H Aiso - [Cited by 1](#)

... nucleus is enhancing by means of **firmware** and hardware ... The **process** is the only one logical entity which ... KMODIFY(PD) primitive can **update** the content of the PD. ...

Proceedings of the first international symposium on ..., 1982 - [portal.acm.org](#) - [portal.acm.org](#)

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Saifun Semiconductor Ltd. has developed a 2-Mbit serial EEPROM memory that fits within an SO-8 package. The company claimed this is an industry first, enabled by its proprietary 2-bit per memory cell technology, which is four times denser than rival companies' EEPROM technology.

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SEGGER Microcontroller Systeme GmbH, Embedded Software Solutions

... Last **Update**: 2004-12-10 [AW]. ... This problem existed since version 3.10n, corrected with version 3.22a. **Task switching** for large memory models corrected **Task** ...
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sMultiTa

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Embedded systems use low level software called firmware, typically stored on non-volatile memory devices such as ROMs, PROMs, EEPROMs, etc. Updates to the firmware are commonly achieved by transferring execution control from the old code image to the new incoming code image, overwriting the old image with the new image, and then transferring execution back again. Performance of embedded systems is enhanced by using a multi-threaded execution. Execution of threads requires a task switch from one thread to the next. During updates, the present invention executes the update routines that exist in the new code image, but transfers execution control back to the old image to access task switching routines.

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S6	327268	(TRANSF??? OR SHAR??? OR SEND??? OR TRANSMIT? OR BROADCAST?? OR COMMUNICAT???) (7N) (EXECUTION??(3N)CONTROL? OR CONTROL? - OR EXECUT???()UPDAT???()ROUTIN???)
S7	499	(OLD()IMAGE?? OR OLD()COD??? OR (OLD OR ORIGINAL? OR PREVIOUS OR EARL??? OR OUTDAT???) (2N) (CODE?? OR SCRIPT?? OR PROGRAM? OR SOFTWARE?? OR EXECUTABLE??) (2N) (IMAGE?? OR MAP?? OR - TOPOLOG??? OR ARCHITECTURE??))
S8	249651	(TASK?? OR JOB??) (3N) (SWITCH??? OR TRAD??? OR EXCHANG???) (-3N)ROUTINE?? OR (TASK?? OR JOB??) (3N) (SWITCH??? OR TRAD??? OR EXCHANG???) OR (TASK?? OR JOB??) (3N)ROUTINE?? OR THREAD?? OR MULTITHREAD OR MULTI()THREAD??
S9	4645	S8(5N) (ACCESS? OR GRANT??? OR PERMIT??? OR ALLOW??? OR AUTHO? OR PERMISSION OR PRIVILEGE?)
S10	93	AU=(GOODMAN, B? OR GOODMAN B?)
S11	1191185	IC=G06F?
S12	54	S11 AND S10
S13	3	S12 AND S1 AND S3
S14	0	S1 AND S3 AND S5 AND S6 AND S7 AND S8
S15	3	S1 AND S3 AND S5
S16	1	S15 NOT S13
S17	0	S16 NOT PY>2002
S18	0	S3 AND S5 AND S6 AND S7 AND S8
S19	0	S1 AND S4 AND S9
S20	8	S1 AND S4 AND S6
S21	5	S20 NOT PY>2002
S22	5	S21 NOT (S13 OR S16)
S23	686	S1 AND S3
S24	275	S23 AND S11
S25	3	S24 AND S5
S26	0	S25 NOT PY>2002
S27	0	S24 AND S7
S28	0	S24 AND S9
S29	1	S24 AND S6 AND S8
S30	0	S29 NOT PY>2002
S31	6	S24 AND S8
S32	3	S31 NOT PY>2002
S33	3	S32 NOT (S22 OR S13 OR S16)
S34	99	S1 AND S4

S35 69 S34 AND S11
 S36 2 S35 AND S8
 S37 1 S36 NOT (S33 OR S22 OR S13 OR S16)
 S38 0 S37 NOT PY>2002

13/3,K/1 (Item 1 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
 (c) 2005 Thomson Derwent. All rts. reserv.

016136185 **Image available**
 WPI Acc No: 2004-294061/200427
 XRPX Acc No: N04-233578

Computer program product has program code which causes computer processor to select update firmware code image that has position dependent coed suitable for replacing determined firmware code image to be updated
 Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)
 Inventor: GOODMAN B G ; KLEM R A; PIERCE T K
 Number of Countries: 002 Number of Patents: 002
 Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20040054883	A1	20040318	US 2002242750	A	20020913	200427 B
CN 1495610	A	20040512	CN 2003156698	A	20030908	200452

Priority Applications (No Type Date): US 2002242750 A 20020913

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20040054883	A1		13	G06F-015/177	
CN 1495610	A			G06F-009/45	

Computer program product has program code which causes computer processor to select update firmware code image that has position dependent coed suitable for replacing determined firmware code image to be updated
 Inventor: GOODMAN B G ...

Abstract (Basic):

... A computer readable program code causes a computer processor to determine which firmware code images stored in rewritable non-volatile memory is to be updated and causes computer processor to select update firmware code image that has position dependent code suitable for replacing determined firmware code image to be updated.
 ... 2) method for updating firmware stored in rewritable non-volatile memory; and...
 ...3) method for supplying firmware for updating firmware stored in rewritable non-volatile memory...
 ...For updating firmware stored in rewritable non-volatile memory of embedded system e.g. modem, answering machine, motor vehicle control, disk drive, tape drive, digital camera, medical...

jump-back address (RADR), at...

...USE - Especially in Private Branch Exchange provided in **embedded system** .

...Title Terms: **ROUTINE** ;
International Patent Class (Main): **G06F-012/02** ...

... **G06F-012/14**

Scitech Files

? show files; ds; save temp; logoff hold

File 2:INSPEC 1969-2005/Feb W3
(c) 2005 Institution of Electrical Engineers

File 6:NTIS 1964-2005/Feb W3
(c) 2005 NTIS, Intl Cpyrght All Rights Res

File 8:Ei Compendex(R) 1970-2005/Jan W3
(c) 2005 Elsevier Eng. Info. Inc.

File 34:SciSearch(R) Cited Ref Sci 1990-2005/Feb W4
(c) 2005 Inst for Sci Info

File 35:Dissertation Abs Online 1861-2005/Feb
(c) 2005 ProQuest Info&Learning

File 65:Inside Conferences 1993-2005/Feb W4
(c) 2005 BLDSC all rts. reserv.

File 94:JICST-EPlus 1985-2005/Jan W3
(c) 2005 Japan Science and Tech Corp(JST)

File 95:TEME-Technology & Management 1989-2005/Jan W4
(c) 2005 FIZ TECHNIK

File 99:Wilson Appl. Sci & Tech Abs 1983-2005/Jan
(c) 2005 The HW Wilson Co.

File 144:Pascal 1973-2005/Feb W3
(c) 2005 INIST/CNRS

File 434:SciSearch(R) Cited Ref Sci 1974-1989/Dec
(c) 1998 Inst for Sci Info

File 583:Gale Group Globalbase(TM) 1986-2002/Dec 13
(c) 2002 The Gale Group

File 603:Newspaper Abstracts 1984-1988
(c) 2001 ProQuest Info&Learning

File 483:Newspaper Abs Daily 1986-2005/Feb 26
(c) 2005 ProQuest Info&Learning

Set	Items	Description
S1	338692	EMBED? OR EMBED?() (SYSTEM?? OR CHIP?? OR DEVICE?? OR LOGIC-??)
S2	544367	LOW?()LEVEL?? OR LOW?()LEVEL?()SOFTWARE?? OR UPDAT???()ROUTINE?? OR ROUTINE?? OR FIRMWARE?? OR FIRM()WARE??
S3	554474	S2 OR (INSTRUCTION?? OR ALGORITHM?? OR PROGRAM?) (3N) (NON(-)VOLATILE?? OR RAM?? OR ROM?? OR PROM?? OR EPROM?? OR DRAM?? - OR FPM?? OR SDRAM?? OR DDR??)
S4	26010	S3(7N) (EXECUT??? OR IMPLEMENT??? OR PERFORM??? OR ACHIEV??? OR COMPLET???)

S5 19496 (NEW()COD??? OR NEW()IMAGE?? OR (NEW??? OR RECENT OR MODERN OR FRESH OR REPLACEMENT OR UPDATE? OR UP()DATE?)(2N)(CODE?? OR SCRIPT?? OR PROGRAM? OR SOFTWARE?? OR EXECUTABLE??)(2N)(-IMAGE?? OR MAP?? OR TOPOLOG??? OR ARCHITECTURE??))

S6 185679 (TRANSF??? OR SHAR??? OR SEND??? OR TRANSMIT? OR BROADCAST?? OR COMMUNICAT??)(7N)(EXECUTION??(3N)CONTROL? OR CONTROL? - OR EXECUT???()UPDAT???()ROUTIN???)

S7 1110 (OLD()IMAGE?? OR OLD()COD??? OR (OLD OR ORIGINAL? OR PREVIOUS OR EARL??? OR OUTDAT???) (2N)(CODE?? OR SCRIPT?? OR PROGRAM? OR SOFTWARE?? OR EXECUTABLE??)(2N)(IMAGE?? OR MAP?? OR - TOPOLOG??? OR ARCHITECTURE??))

S8 62364 (TASK?? OR JOB??)(3N)(SWITCH??? OR TRAD??? OR EXCHANG???) (-3N)ROUTINE?? OR (TASK?? OR JOB??)(3N) (SWITCH??? OR TRAD??? OR EXCHANG???) OR (TASK?? OR JOB??)(3N)ROUTINE?? OR THREAD?? OR MULTITHREAD OR MULTI()THREAD??

S9 2149 S8(5N)(ACCESS? OR GRANT??? OR PERMIT??? OR ALLOW??? OR AUTHO? OR PERMISSION OR PRIVILEGE?)

S10 1314 AU=(GOODMAN, B? OR GOODMAN B?)

S11 0 S10 AND S1

S12 0 S1 AND S3 AND S5 AND S6 AND S7 AND S8

S13 0 S1 AND S3 AND S4 AND S5 AND S7

S14 21 S1 AND S3 AND S5

S15 19 S14 NOT PY>2002

S16 12 RD (unique items)

S17 0 S1 AND S3 AND S7

S18 2 S1 AND S3 AND S6 AND S8

S19 2 S18 NOT S16

S20 1 S19 NOT PY>2002

S21 2 S1 AND S4 AND S9

S22 2 S21 NOT (S20 OR S16)

S23 2 S22 NOT PY>2002

S24 14 S1(S)S4(S)S6

S25 14 S24 NOT (S23 OR S20 OR S16)

S26 9 S25 NOT PY>2002

16/3,K/1 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

7473103 INSPEC Abstract Number: C2003-01-6150C-018

Title: Vista: a system for interactive code improvement

Author(s): Wankang Zhao; Baosheng Cai; Whalley, D.; Bailey, M.W.; Van Engelen, R.; Xin Yuan; Hiser, J.D.; Davidson, J.W.; Gallivan, K.; Jones, D.L.

Author Affiliation: Dept. of Comput. Sci., Florida State Univ., Tallahassee, FL, USA

Journal: SIGPLAN Notices Conference Title: SIGPLAN Not. (USA) vol.37, no.7 p.155-64

Publisher: ACM,

Publication Date: July 2002 Country of Publication: USA

CODEN: SINODQ ISSN: 0362-1340

SICI: 0362-1340(200207)37:7L.155:VSIC;1-L

Material Identity Number: S202-2002-008

Conference Title: Joint Conference on Languages, Compilers and Tools for

control

...

...the same, or vary only slightly, between systems. If these common functions could be extracted, **implemented** as a set of extensible low

level building blocks, and made readily available to system designers, development schedules could be accelerated and...

...again for each project. The paper discusses the use of DII COE for weapon system **embedded** computing.

26/3,K/9 (Item 1 from file: 144)
DIALOG(R)File 144:Pascal
(c) 2005 INIST/CNRS. All rts. reserv.

14756171 PASCAL No.: 00-0434057
Computer vision Camera with **embedded FPGA processing**
Machine vision applications in industrial inspection VIII : San Jose
CA,
24-26 January 2000
LECERF A; OUELLET D; ARIAS-ESTRADA M
TOBIN Kenneth W, ed
Computer Vision and Systems Laboratory, Universite Laval, Quebec, PQ,
Canada
International Society for Optical Engineering, Bellingham WA, United
States
Machine vision applications in industrial inspection. Conference, 8
(San
Jose CA USA) 2000-01-24
Journal: SPIE proceedings series, 2000, 3966 299-308
Language: English

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...a CMOS image sensor, an FPGA device, two memory banks, and an
embedded
PC for **communication** and **control** tasks. The FPGA device is a
medium
size one equivalent to 25,000 logic gates...
... ISA interface, and an imager interface. The camera can be accessed
for
architecture programming, data **transfer** , and **control** through
an
Ethernet link from a remote computer. A hardware architecture
can be
defined in...
?

Software File

? show files; ds; save temp; logoff hold
File 256:TecInfoSource 82-2005/Jan

(c) 2005 Info.Sources Inc

Set	Items	Description
S1	1799	EMBED? OR EMBED?() (SYSTEM?? OR CHIP?? OR DEVICE?? OR LOGIC-??)
S2	701	LOW?()LEVEL?? OR LOW?()LEVEL?()SOFTWARE?? OR UPDAT???()ROUTINE?? OR ROUTINE?? OR FIRMWARE?? OR FIRM()WARE??
S3	746	S2 OR (INSTRUCTION?? OR ALGORITHM?? OR PROGRAM?) (3N) (NON(-)VOLATILE?? OR RAM?? OR ROM?? OR PROM?? OR EPROM?? OR DRAM?? - OR FPM?? OR SDRAM?? OR DDR??)
S4	36	S3(7N) (EXECUT??? OR IMPLEMENT??? OR PERFORM??? OR ACHIEV??? OR COMPLET???)
S5	128	(NEW()COD??? OR NEW()IMAGE?? OR (NEW??? OR RECENT OR MODERN OR FRESH OR REPLACEMENT OR UPDATE? OR UP()DATE?) (2N) (CODE?? OR SCRIPT?? OR PROGRAM? OR SOFTWARE?? OR EXECUTABLE??) (2N) (-IMAGE?? OR MAP?? OR TOPOLOG??? OR ARCHITECTURE??))
S6	312	(TRANSF??? OR SHAR??? OR SEND??? OR TRANSMIT? OR BROADCAST?? OR COMMUNICAT???) (7N) (EXECUTION?? (3N) CONTROL? OR CONTROL? - OR EXECUT???()UPDAT???()ROUTIN???)
S7	12	(OLD()IMAGE?? OR OLD()COD??? OR (OLD OR ORIGINAL? OR PREVIOUS OR EARL??? OR OUTDAT???) (2N) (CODE?? OR SCRIPT?? OR PROGRAM? OR SOFTWARE?? OR EXECUTABLE??) (2N) (IMAGE?? OR MAP?? OR -TOPOLOG??? OR ARCHITECTURE??))
S8	343	(TASK?? OR JOB??) (3N) (SWITCH??? OR TRAD??? OR EXCHANG???) (-3N) ROUTINE?? OR (TASK?? OR JOB??) (3N) (SWITCH??? OR TRAD??? OR EXCHANG???) OR (TASK?? OR JOB??) (3N) ROUTINE?? OR THREAD?? OR MULTITHREAD OR MULTI()THREAD??
S9	33	S8(5N) (ACCESS? OR GRANT??? OR PERMIT??? OR ALLOW??? OR AUTHO? OR PERMISSION OR PRIVILEGE?)
S10	0	AU=(GOODMAN, B? OR GOODMAN B?)
S11	1	S5(S)S7
S12	1	S11 NOT PY>2002
S13	3	S1(S)S9
S14	3	S13 NOT PY>2002
S15	3	S14 NOT S12
S16	2	S(S)S6(S)S8
S17	2	S16 NOT (S15 OR S12)
S18	0	S4(S)S7
S19	0	S4(S)S5

12/3,K/1

DIALOG(R) File 256:TecInfoSource

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00146501 DOCUMENT TYPE: Review

PRODUCT NAMES: Primary Response (149446)

TITLE: Intrusion Protection: Primary Response

AUTHOR: Bobbitt, Mike

SOURCE: Information Security, v6 n3 p76(1) Mar 2003

ISSN: 1096-8903

HOME PAGE: <http://www.infosecuritymag.com>

RECORD TYPE: Review

REVIEW TYPE: Review

GRADE: A

REVISION DATE: 20030830

17/3,K/2

DIALOG(R)File 256:TecInfoSource
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00144254 DOCUMENT TYPE: Review

PRODUCT NAMES: NU-FAST (162132); WISTOM (162141)

TITLE: Sound waves shake up fiber-optic components

AUTHOR: Freeman, Tami

SOURCE: FiberSystems International, v3 n7 p21(3) Nov/Dec 2002

HOME PAGE: <http://www.fibers.org>

RECORD TYPE: Review

REVIEW TYPE: Product Analysis

GRADE: Product Analysis, No Rating

REVISION DATE: 20030730

Nuonics' Nu-Fast and Acreo' s WISTOM are highlighted in a discussion of acousto-optic technology. NuFast is the acousto-optics...

...the fiber. Nuonics' data sheet says the device has a response time of 1.5mu/ s for the high dynamic range model. Nu-Fast also process up to 2W of continuous-wave optical power. The high-end VOA is optimal for applications such as adaptive control in laser transmitters and optical amplifiers. The NuFast E model provides an insertion loss of under 1.5dB...

...fiber acousto-optic filters, with broadband spectral equalization handled by a series of piezoelectric transducers threaded onto the fiber.
?

Patent Full Text

? show files; ds; save temp; logoff hold
File 348:EUROPEAN PATENTS 1978-2005/Feb W03
(c) 2005 European Patent Office
File 349:PCT FULLTEXT 1979-2002/UB=20050224,UT=20050217
(c) 2005 WIPO/Univentio

Set	Items	Description
S1	115141	EMBED? OR EMBED?() (SYSTEM?? OR CHIP?? OR DEVICE?? OR LOGIC-??)
S2	231496	LOW?()LEVEL?? OR LOW?()LEVEL?()SOFTWARE?? OR UPDAT???()ROUTINE?? OR ROUTINE?? OR FIRMWARE?? OR FIRM()WARE??

S3 247345 S2 OR (INSTRUCTION?? OR ALGORITHM?? OR PROGRAM?) (3N) (NON(-
)VOLATILE?? OR RAM?? OR ROM?? OR PROM?? OR EPROM?? OR DRAM?? -
 OR FPM?? OR SDRAM?? OR DDR??)
 S4 43793 S3 (7N) (EXECUT??? OR IMPLEMENT??? OR PERFORM??? OR ACHIEV???
 OR COMPLET???)
 S5 7573 (NEW()COD??? OR NEW()IMAGE?? OR (NEW??? OR RECENT OR MODE-
 RN OR FRESH OR REPLACEMENT OR UPDATE? OR UP()DATE?) (2N) (CODE??
 OR SCRIPT?? OR PROGRAM? OR SOFTWARE?? OR EXECUTABLE??) (2N) (-
 IMAGE?? OR MAP?? OR TOPOLOG??? OR ARCHITECTURE??))
 S6 180900 (TRANSF??? OR SHAR??? OR SEND??? OR TRANSMIT? OR BROADCAST-
 ?? OR COMMUNICAT???) (7N) (EXECUTION?? (3N) CONTROL? OR CONTROL? -
 OR EXECUT??? () UPDAT??? () ROUTIN???)
 S7 2510 (OLD()IMAGE?? OR OLD()COD??? OR (OLD OR ORIGINAL? OR PREV-
 IIOUS OR EARL??? OR OUTDAT???) (2N) (CODE?? OR SCRIPT?? OR PROG-
 RAM? OR SOFTWARE?? OR EXECUTABLE??) (2N) (IMAGE?? OR MAP?? OR -
 TOPOLOG??? OR ARCHITECTURE??))
 S8 161077 (TASK?? OR JOB??) (3N) (SWITCH??? OR TRAD??? OR EXCHANG???) (-
 3N) ROUTINE?? OR (TASK?? OR JOB??) (3N) (SWITCH??? OR TRAD??? OR
 EXCHANG???) OR (TASK?? OR JOB??) (3N) ROUTINE?? OR THREAD?? OR
 MULTITHREAD OR MULTI() THREAD??
 S9 9721 S8 (5N) (ACCESS? OR GRANT??? OR PERMIT??? OR ALLOW??? OR AUT-
 HO? OR PERMISSION OR PRIVILEGE?)
 S10 23 AU=(GOODMAN, B? OR GOODMAN B?)
 S11 139482 IC=G06F?
 S12 8 S10 AND S11
 S13 2 S12 AND S1
 S14 2 S1(S)S3(S)S5(S)S6(S)S7(S)S8
 S15 2 S14 NOT S13
 S16 2 S15 NOT PY>2002
 S17 10 S1(S)S3(S)S5(S)S6(S)S7
 S18 8 S17 NOT S16
 S19 8 S18 AND S11
 S20 8 S19 NOT PY>2002
 S21 8 IDPAT (sorted in duplicate/non-duplicate order)
 S22 8 IDPAT (primary/non-duplicate records only)
 S23 0 S1(7N)S3(5N)S5(5N)S7
 S24 1 S1(7N)S3(5N)S5
 S25 1 S24 NOT (S22 OR S16 OR S13)
 S26 0 S25 NOT PY>2002
 S27 3 S1(7N)S3(5N)S7
 S28 3 S27 NOT(S22 OR S16 OR S13)
 S29 1 S28 NOT PY>2002
 S30 0 S1(3N)S4(3N)S9
 S31 2 S1(3N)S4(3N)S6
 S32 2 S31 NOT (S22 OR S16 OR S13)
 S33 0 S32 NOT PY>2002
 S34 0 S1(20N)S5(20N)S7(20N)S3

13/3,K/1 (Item 1 from file: 349)
 DIALOG(R)File 349:PCT FULLTEXT
 (c) 2005 WIPO/Univentio. All rts. reserv.

01140970 **Image available**

BACKUP FIRMWARE IN A DISTRIBUTED SYSTEM
MICROLOGICIEL DE SAUVEGARDE DANS UN SYSTEME REPARTI

Patent Applicant/Assignee:

INTERNATIONAL BUSINESS MACHINES CORPORATION, New Orchard Road,
 Armonk,

New York 10504, US, US, US (Residence), US (Nationality), (For all

Business Files

? show files; ds; save temp; logoff hold

File 9:Business & Industry(R) Jul/1994-2005/Mar 02
(c) 2005 The Gale Group

File 15:ABI/Inform(R) 1971-2005/Mar 03
(c) 2005 ProQuest Info&Learning

File 16:Gale Group PROMT(R) 1990-2005/Mar 03
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File 20:Dialog Global Reporter 1997-2005/Mar 03
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File 47:Gale Group Magazine DB(TM) 1959-2005/Mar 03
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File 75:TGG Management Contents(R) 86-2005/Feb W3
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File 80:TGG Aerospace/Def.Mkts(R) 1982-2005/Mar 03
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File 88:Gale Group Business A.R.T.S. 1976-2005/Mar 02
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File 98:General Sci Abs/Full-Text 1984-2004/Dec
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File 112:UBM Industry News 1998-2004/Jan 27
(c) 2004 United Business Media

File 141:Readers Guide 1983-2004/Sep
(c) 2004 The HW Wilson Co

File 148:Gale Group Trade & Industry DB 1976-2005/Mar 03
(c)2005 The Gale Group

File 160:Gale Group PROMT(R) 1972-1989
(c) 1999 The Gale Group

File 275:Gale Group Computer DB(TM) 1983-2005/Mar 03
(c) 2005 The Gale Group

File 264:DIALOG Defense Newsletters 1989-2005/Mar 01
(c) 2005 The Dialog Corp.

File 484:Periodical Abs Plustext 1986-2005/Feb W4
(c) 2005 ProQuest

File 553:Wilson Bus. Abs. FullText 1982-2004/Dec
(c) 2005 The HW Wilson Co

File 570:Gale Group MARS(R) 1984-2005/Mar 03
(c) 2005 The Gale Group

File 608:KR/T Bus.News. 1992-2005/Mar 03
(c)2005 Knight Ridder/Tribune Bus News

File 620:EIU:Viewswire 2005/Mar 02
(c) 2005 Economist Intelligence Unit

File 613:PR Newswire 1999-2005/Mar 03
(c) 2005 PR Newswire Association Inc

File 621:Gale Group New Prod.Annou.(R) 1985-2005/Mar 03
(c) 2005 The Gale Group

File 623:Business Week 1985-2005/Mar 03
(c) 2005 The McGraw-Hill Companies Inc

File 624:McGraw-Hill Publications 1985-2005/Mar 03
(c) 2005 McGraw-Hill Co. Inc

File 634:San Jose Mercury Jun 1985-2005/Mar 02
(c) 2005 San Jose Mercury News
File 635:Business Dateline(R) 1985-2005/Mar 03
(c) 2005 ProQuest Info&Learning
File 636:Gale Group Newsletter DB(TM) 1987-2005/Mar 03
(c) 2005 The Gale Group
File 647:CMP Computer Fulltext 1988-2005/Feb W2
(c) 2005 CMP Media, LLC
File 696:DIALOG Telecom. Newsletters 1995-2005/Mar 02
(c) 2005 The Dialog Corp.
File 674:Computer News Fulltext 1989-2005/Feb W4
(c) 2005 IDG Communications
File 810:Business Wire 1986-1999/Feb 28
(c) 1999 Business Wire
File 813:PR Newswire 1987-1999/Apr 30
(c) 1999 PR Newswire Association Inc
File 587:Jane`s Defense&Aerospace 2005/Feb W2
(c) 2005 Jane`s Information Group

Set	Items	Description
S1	756513	EMBED? OR EMBED?() (SYSTEM?? OR CHIP?? OR DEVICE?? OR LOGIC-??)
S2	1703013	LOW?()LEVEL?? OR LOW?()LEVEL?()SOFTWARE?? OR UPDAT???()ROUTINE?? OR ROUTINE?? OR FIRMWARE?? OR FIRM()WARE??
S3	1772458	S2 OR (INSTRUCTION?? OR ALGORITHM?? OR PROGRAM?) (3N) (NON(-)VOLATILE?? OR RAM?? OR ROM?? OR PROM?? OR EPROM?? OR DRAM?? - OR FPM?? OR SDRAM?? OR DDR??)
S4	76240	S3(7N) (EXECUT??? OR IMPLEMENT??? OR PERFORM??? OR ACHIEV??? OR COMPLET???)
S5	101095	(NEW()COD??? OR NEW()IMAGE?? OR (NEW??? OR RECENT OR MODERN OR FRESH OR REPLACEMENT OR UPDATE? OR UP()DATE?) (2N) (CODE?? OR SCRIPT?? OR PROGRAM? OR SOFTWARE?? OR EXECUTABLE??) (2N) (-IMAGE?? OR MAP?? OR TOPOLOG??? OR ARCHITECTURE??))
S6	441960	(TRANSF??? OR SHAR??? OR SEND??? OR TRANSMIT? OR BROADCAST?? OR COMMUNICAT???) (7N) (EXECUTION??(3N)CONTROL? OR CONTROL? - OR EXECUT???()UPDAT???()ROUTIN???)
S7	11584	(OLD()IMAGE?? OR OLD()COD??? OR (OLD OR ORIGINAL? OR PREVIOUS OR EARL??? OR OUTDAT???) (2N) (CODE?? OR SCRIPT?? OR PROGRAM? OR SOFTWARE?? OR EXECUTABLE??) (2N) (IMAGE?? OR MAP?? OR -TOPOLOG??? OR ARCHITECTURE??))
S8	351096	(TASK?? OR JOB??) (3N) (SWITCH??? OR TRAD??? OR EXCHANG???) (-3N)ROUTINE?? OR (TASK?? OR JOB??) (3N) (SWITCH??? OR TRAD??? OR EXCHANG???) OR (TASK?? OR JOB??) (3N)ROUTINE?? OR THREAD?? OR MULTITHREAD OR MULTI()THREAD??
S9	10716	S8(5N) (ACCESS? OR GRANT??? OR PERMIT??? OR ALLOW??? OR AUTHO? OR PERMISSION OR PRIVILEGE?)
S10	744	AU=(GOODMAN, B? OR GOODMAN B?)
S11	0	S1(S)S3(S)S5(S)S6(S)S7(S)S8
S12	0	S1(S)S3(S)S5(S)S6(S)S7
S13	0	S1(S)S3(S)S5(S)S7
S14	21	S1(S)S3(S)S5
S15	16	RD (unique items)
S16	15	S15 NOT PY>2002
S17	0	S1(S)S3(S)S7
S18	0	S1(S)S4(S)S9
S19	0	S1(S)S4(S)S9
S20	776	S5(S)S7
S21	10	S20(S)S3
S22	8	RD (unique items)
S23	8	S22 NOT PY>2002
S24	8	S23 NOT S16